

CPLD BASED CONTROLLER FOR SINGLE PHASE INVERTERS

SUNAINI BIN SAIMAN

MASTER OF ENGINEERING (ELECTRICAL ENERGY AND POWER SYSTEM)



FACULTY OF ENGINEERING

UNIVERSITY OF MALAYA

KUALA LUMPUR

JANUARY, 2007

PERPUSTAKAAN UTHM



01 130529

30000002354041



PTTA UTHM
PERPUSTAKAAN TUNKU TUN AMINAH

CPLD BASED CONTROLLER FOR SINGLE PHASE INVERTERS

SUHAIMI BIN SAIMAN

This thesis is submitted as partial fulfillment of the requirements for the award of the
Master of Engineering (Electrical Energy and Power System)



PTTAUTHIM
PERPUSTAKAAN TUNKU TUN AMINAH

Faculty of Engineering

University of Malaya

January, 2007

UNIVERSITY MALAYA

UNIVERSITY MALAYA

ORIGINAL LITERARY WORK DECLARATION

Name of Candidate:

SUHAIMI BIN SAIMAN (I.C/Passport No: 710627-01-5967)

Registration/Matric No:

KGD 020002

Name of Degree:

MASTER OF ENGINEERING

Title of Project Paper/Research Report/Dissertation/Thesis ("this Work"):

CPLD BASED CONTROLLER FOR SINGLE PHASE INVERTERS

Field of Study:

ELECTRICAL ENERGY AND POWER SYSTEM

I do solemnly and sincerely declare that:

- (1) I am the sole author/writer of this work.
- (2) This Work is original.
- (3) Any use of any work in which copyright exists was done by way of fair dealing for permitted purposes and any excerpt or extract from, or reference to or reproduction of any copyright work has been disclosed expressly and sufficiently and the title of the of the Work and its authorship have been acknowledged in this Work.

- (4) I do not have any actual knowledge nor ought I reasonably to know that the making of this work constitutes an infringement of any copyright work.
- (5) I hereby assign all every rights in the copyright to this Work to the University of Malaya (“UM”), who henceforth shall be the copyright in this Work and that any reproduction or use in any form or by any means whatsoever is prohibited without the written consent of UM having been first had and obtained.
- (6) I am fully aware that if in the course of making this Work I have infringed any copyright whether intentionally or otherwise, I may be subject to legal action or any other action as may be determined by UM.

Candidate's Signature

Date

Subscribed and solemnly declared before,

Witness's Signature

Date

Name:

Designation:

ACKNOWLEDGEMENT

In the name of Allah, the most Gracious and most Compassionate

First of all, I would like to thank Allah Almighty for blessing and giving me strength to accomplish this thesis. I also would like to acknowledge Dr. Saad Mekhilef for his continuous guidance, help and encouragement through out the work. Many of my accomplishments would not been realize without his dedication to work hard. The grateful thank to Universiti Tun Hussein Onn Malaysia (UTHM) in providing me the financial assistant along the period of my study in this university.

Special thank and appreciation goes to all my friends, technicians whose in charge the laboratory, and peoples either in UM or UTHM for their help at various occasion.

Lastly, my warmest thanks go to my parent and parent-in-law for their support. My highest appreciation goes to my loving wife, Amelia Kamijan, and all my loving children, Nurin Syaza Amani, Muhammad Ammar Syazwi and Muhammand Akif Syahmi for their unconditional support and love that continuously fed my strength desire to succeed.

ABSTRACT

The DC-AC converter, also known as inverter, converts DC power to AC power at desired output voltage and frequency. The DC power input to the inverter is obtained from an existing power supply. Nowadays inverters use high power switching transistors either IGBT's and/or MOSFETs. In addition, the voltage and frequency of the source can be adjustable. These single phase inverters and their operating principles are analyzed in detail.

In this project, a full-bridge, single phase inverter that uses a digital Pulse Width Modulation (PWM) to control the power switches at 18 kHz was constructed. The concept of PWM with different strategies for inverters is described. A type of filter is used to improve the distortion in the output waveform.

A design and implementation of PWM by using complex programmable logic device (CPLD) from Altera MaxPlus II is constructed and programmed. The involved software, hardware, and suitable algorithm to implement and generate the PWM are developed in details. To verify the significant of this single phase inverter, the output voltage will be tested with resistive load and inductive load.

TABLE OF CONTENTS

ORIGINAL LITERARY WORK DECLARATION	ii
ACKNOWLEDGEMENT.....	iv
ABSTRACT.....	v
TABLE OF CONTENTS	vi
LIST OF TABLES.....	xi
LIST OF FIGURES.....	xii
LIST OF SYMBOL AND ABBREVIATIONS.....	xvi
LIST OF APPENDICES.....	xix
CHAPTER 1 INTRODUCTION	1
1.0 Introduction to Power Electronics	1
1.1 Significance of Power Electronics	2
1.2 Power Semiconductor Devices	2
1.3 Power Converters	5
1.4 Pulse Width Modulation	8
1.5 Harmonics	9
1.5 Objectives of the Project	10
1.6 Organization of the Dissertation	11
References	12

CHAPTER 2 LITERATURE REVIEW 14

2.0	Introduction	14
2.1	Inverter Topologies	15
2.2	Voltage Control in Inverters	15
2.3	Analog Circuits	18
2.4	Digital Control Circuit	19
2.5	Digital PWM Controller	20
2.5.1	Digital Design Approaches	22
2.5.2	Impact of Digital Control	22
2.6	High Frequency Switching	23
2.7	Complex Programmable Logic Device (CPLD)	25
	References	27

CHAPTER 3 PULSE WIDTH MODULATION 33

3.0	Introduction	33
3.1	Sinusoidal PWM	36
3.2	Digital PWM Technique	40
3.3	Symmetrical Sampling PWM	40
3.4	Asymmetrical Sampling PWM	42
	References	44

CHAPTER 4	PROGRAMMABLE LOGIC DEVICE	46
4.0	Introduction	46
4.1	Digital Logic Design	47
4.1.1	Techniques of Traditional Digital Circuit Design	49
4.1.2	Integrated Digital Logic Design	50
4.2	Comparison of Traditional and Integrated Digital Design	51
4.3	FPGAs AND CPLDs	53
4.4	Altera CPLD	55
4.5	Significance of CPLD	57
	References	58
CHAPTER 5	MODELLING AND SIMULATION OF DIGITAL PWM INVERTER	59
5.0	Introduction	59
5.1	Generating PWM	60
5.2	The Carrier Waveform	63
5.3	The Sinusoidal Waveform	66
5.4	Symmetrical Sinusoidal PWM	71
5.5	Asymmetrical Sinusoidal PWM	72
5.6	Comparator	73
5.7	Adjusting Modulation Index	74



PTTA UTHM
PERPUSTAKAAN TUNKU TUN AMINAH

5.8	Generated PWM	76
	References	80

CHAPTER 6 HARDWARE IMPLEMENTATION 82

6.0	Introduction	82
6.1	Inverter Circuit	83
6.2	Altera MaxPlus II University Program (UP) Board	84
6.3	Using Altera FLEX 10K	85
6.4	The Modulator	86
6.5	Gate Driver	86
6.6	LC Filter	87
6.7	DC-Link Capacitor	89
6.8	Snubber Circuit	90
	References	91

CHAPTER 7 SIMULATION AND EXPERIMENTAL RESULTS 92

7.0	Introduction	92
7.1	Orcad Simulation	92
7.2	Hardware Experimental	96
7.2.1	Results of Altera MaxPlus II Simulation	97
7.2.2	Results of Hardware Experimental	98

7.3	Total Harmonic Distortion (THD)	103
CHAPTER 8 CONCLUSION		105
8.0	Concluding Remarks	105
8.1	Author's Contribution	106
8.2	Area for Future Works	107
Appendix A		108
Appendix B		113



PTTA UTHM
PERPUSTAKAAN TUNKU TUN AMINAH

LIST OF TABLES

No. of tables	Titles	Pages
Table 4.1	Comparison of PLD, Standard or Discrete Logic, and Full- Custom IC.	49
Table 4.2 (a)	Comparisons of traditional and integrated digital by tools.	51
Table 4.2 (b)	Comparisons of traditional and integrated digital by elements.	52
Table 4.2(c)	Comparisons of traditional and integrated digital by circuit design flow.	52
Table 4.2(d)	Comparisons of traditional and integrated digital by study requirement.	53



PT TA UTHM
PERPUSTAKAAN TUNKU TUN AMINAH

LIST OF FIGURES

No. of figures	Titles	Pages
Figure 2.1	DC to AC conversion system: (a) single phase, (b) and three phase.	15
Figure 2.2	Analog circuit design trade-offs.	19
Figure 3.1	PWM signals of varying duty cycles.	34
Figure 3.2	Analog carrier based PWM technique.	36
Figure 3.3	Saw-tooth PWM.	37
Figure 3.4	Sinusoidal Pulse Width Modulation (SPWM).	38
Figure 3.5	Determining the width of SPWM.	39
Figure 3.6	Symmetrical PWM.	41
Figure 3.7	Asymmetrical PWM.	42
Figure 4.1	Digital Logic Designs.	47
Figure 4.2	MAXPLUS II functional diagram and design flow.	55
Figure 5.1	PWM block diagram.	61
Figure 5.2	PWM design using ALTERA MAX+Plus II.	62
Figure 5.3	Determining the U/D counter clock frequency.	63
Figure 5.4:	Generating carrier waveform.	64
Figure 5.5	VHDL codes for U/D counter.	65
Figure 5.6	Sampling symmetrical SPWM.	67

Figure 5.7	Sampling asymmetrical SPWM.	68
Figure 5.8 (a)	Graph obtained from data sampled.	69
Figure 5.8 (b)	Sample data from MathCAD.	70
Figure 5.9	Symmetrical SPWM.	71
Figure 5.10	Asymmetrical SPWM.	72
Figure 5.11	VHDL code for comparator.	74
Figure 5.12	VHDL code for multiplier.	75
Figure 5.13	Single Phase full bridge inverter.	76
Figure 5.14	PWM switching pattern.	77
Figure 5.15	PWM pattern with MaxPlus II simulation.	78
Figure 6.1	Block diagram of overall interconnection of PWM and inverter.	82
Figure 6.2	Full bridge inverter circuit.	83
Figure 6.3	The outline of UP board unit	84
Figure 6.4	Schematic diagram of gate driver	87
Figure 7.1	Schematic diagram of single phase full bridge inverter.	93
Figure 7.2	Schematic diagram of PWM.	93
Figure 7.3(a)	Sine waveform and triangle waveform.	94
Figure 7.3(b)	PWM signal.	95
Figure 7.4(a)	The output inverter for resistive load.	95
Figure 7.4(b)	The output inverter for inductive load.	96
Figure 7.5	Digital PWM signal.	97

Figure 7.6	Sine wave signal.	97
Figure 7.7	Carrier frequency.	98
Figure 7.8	The PWM signal.	99
Figure 7.9(a)	Modulation index, $M = 0.1$ (10% of duty cycle).	99
Figure 7.9(b)	Modulation index, $M = 0.5$ (50% of duty cycle).	100
Figure 7.9(c)	Modulation index, $M = 0.9$ (90% of duty cycle).	100
Figure 7.10	Unfiltered output voltage waveform.	101
Figure 7.11	Filtered output voltage waveform.	101
Figure 7.12(a)	With resistive load.	102
Figure 7.12(b)	With inductive load.	102
Figure 7.13	Harmonic spectral for output resistive load after filter, (a) simulated, (b) experimental.	103
Figure 7.14	Harmonic spectral for output inductive load after filter, (a) Simulation, (b) Experimental.	104
Figure B1	Hardware experimental setup	113
Figure B2	ALTERA FLEX 10K University Program Board.	114
Figure B3	Switching devices with heat sink.	115
Figure B4	Snubber unit.	116
Figure B5	Driver unit.	117
Figure B6	Low-pass filter.	118
Figure B7	Fuse board.	119
Figure B8	Resistive load and inductive load.	120

Figure B9	Altera programming in PC.	121
Figure B10	Waveform captured using oscilloscope.	122



PTTA UTHM
PERPUSTAKAAN TUNKU TUN AMINAH

LIST OF SYMBOLS AND ABBREVIATIONS

Symbols:

μ	Micro (10^{-6})
Ω	Ohm
π	Pi (180°)
Σ	Sum
ω	Omega
ϕ	Phase displacement
C	Capacitance
f	Frequency
k	Kilo (10^3)
L	Inductor
m	Mili (10^{-3})
M	Mega (10^6)
T	Switching period



PTTA UTHM
PERPUSTAKAAN TUNKU TUN AMINAH

Abbreviations:

AC	Alternating Current
DC	Direct Current
THD	Total Harmonic Distortion
UPS	Uninterruptible Power Supply
CVCF	Constant Voltage and Constant Frequency
KV	Kilo-Volt
BJT	Bipolar Junction Transistor
TTL	Transistor-transistor Logic
MOS	Metal Oxide Semiconductor
CMOS	Complementary Metal Oxide Semiconductor
MCT	MOS-controlled Thyristor
IGBT	Insulated Gate Bipolar Transistor
MOSFET	Metal Oxide Semiconductor Field Effect Transistor
PWM	Pulse Width Modulation
ASIC	Application Specific Integreter
DSP	Digital Signal Processor
PAL	Programmable Array Logic
GAL	General Array Logic
CPLD	Complex Programmable Logic Device
FPGA	Field Programmable Gate Array.
VHDL	Very High Digital language.

HVDC	High Voltage Direct Current
M_a	Modulation Index
GTO	Gate Turn-Off
EDA	Electronic Design Automatic
SRAM	Static Random Access Memory
UP	University Program
LED	Light-emitting diode
ADC	Analog to Digital Converter
PID	Proportional Integral Derivative
RAM	Random Access Memory
RMS	Root mean square
THD	Tottal Harmonics
U/D	Up Dawn
MOD	Modulus
VFI	Voltage Fed inverter
CFI	Current Fed inverter



PERPUSTAKAAN TUNKU TUN AMINAH

LIST OF APPENDICES

No. of appendix	Titles	Pages
Appendix A	The MathCAD data.	107
Appendix B	The pictures of hardware implementation.	112



PTTA UTHM
PERPUSTAKAAN TUNKU TUN AMINAH

CHAPTER 1

INTRODUCTION



PTTA UTHM
PERPUSTAKAAN TUNKU TUN AMINAH

CHAPTER 1

INTRODUCTION

1.0 Introduction to power electronics

Power electronics can be defined as the use of electronic devices to control and convert electric power. Power electronics combine power, electronics and control. It may be defined as the applications of solid-state electronics for the control and conversion of electric power. It is based on the switching of power semiconductor devices whose power handling capabilities and switching speeds have improved tremendously over the years. The switching characteristics of power devices permit the control and conversion of electric power from one to others. Versatile circuit topologies can be found in the power electronics for different applications [1.1].

Power electronics have already found an important place in modern technology and are now used in a great variety of high-power products, including motor controls, power supplies and High Voltage Direct Current (VHDC) systems [1.1].

Power electronics is the technology associated with the efficient conversion, control and conditioning of electric power by static means from its available input form into the desired electrical output form. The goal of power electronics is to control the flow of energy from an electrical source to an electrical load with high efficiency, high availability, high reliability, small size, light weight, and low cost [1.7].

1.1 Significance of power electronics

The demand for control of electric power exists for many years. The generation, transmission, and distribution of electric power are almost AC today, but industry, transportation, agriculture, and everyday life often demand DC power. In any technically and economically defined situation, it is necessary to provide the most suitable form of energy to meet the demand of user [1.3].

Power electronics can process the power in two forms, AC and DC. For AC, it can process by magnitude and frequency and for DC by only magnitude [1.4].

1.2 Power semiconductor devices

Power semiconductor devices play a crucial role in the regulation and distribution of power and energy in the world. In power electronics, it is well recognized that improvement in system performance in terms of efficiency, size and weight are driven by enhancement made in semiconductor device characteristics. Overall system reliability and efficiency depends on the quality of semiconductor switches and how these devices are used. Throughout the last 50 years, power electronics technology mostly evolved with the availability of new and improved power semiconductor devices. With the development of power semiconductor technology, the power handling capabilities and the switching speed of the power devices have improved tremendously [1.1].

The most important elements of power electronics are the power semiconductor devices. A wide variety of devices are available, which made it difficult to compare them. Their

numerical ratings that include voltage ratings, current ratings and switching frequency vary widely from one device type to another and also there may be a wide variation of ratings within one type of device. Manufacturer's specification sheets show a considerable amount of tolerance of parameters for a particular device [1.5].

The first power semiconductor switches since the evolution of power electronics systems was dominated by thyristors, developed in the 1950s. The first thyristor SCR was developed in late 1957. Since then, there have been tremendous advances in the power semiconductor devices. Until 1970s, the conventional thyristors had been exclusively used for power control in industrial applications [1.1].

Presently available power semiconductor devices can be classified into three groups according to their degree of controllability [1.3]:

- (i) Diodes – ON and OFF states controlled by the power circuit
- (ii) Thyristors – latched on by a control signal but must be turned OFF by power circuit.
- (iii) Controllable switches – turned ON and OFF by control signals.

The controllable switch category includes several device types including bipolar junction transistors (BJTs), metal-oxide-semiconductor field effect transistors (MOSFETs), gate turn off (GTO) thyristor, and insulated gate bipolar transistors (IGBTs) [1.3].

Ever since their introduction in the 1950's, bipolar junction transistors (BJTs) were favored for low and medium power applications because of their faster switching capability. High-power bipolar transistors are commonly used in power converters at a

frequency below 10 kHz and are effectively applied in the power rating up to 1200V and 400A. The bipolar transistor requires a high base current to turn on, and has relatively slow turn off characteristics and is liable for thermal runaway due to a negative temperature co-efficient. Bipolar transistor was the only 'real' power transistor until the MOSFET came along in the 1970s for low power applications and Insulated Gate Bipolar Transistor (IGBT) in the 1980s for medium power applications [1.1].

In the 1970s, the highly anticipated MOS (metal oxide-semiconductor) gate power device was introduced. Later, the power MOSFET (MOS field-effect transistor) was invented and has become the dominant device technology for low power applications. Production of the power MOSFET utilized the cutting-edge semiconductor technology of those days. Power MOSFET has very high input impedance in the steady state due to its metal-oxide-semiconductor (MOS) gate structure, and is classified as a voltage-controlled device. It is used in high-speed power converters and is available at a relatively low power rating in the range of 1000V and 100A at a frequency range of several tens of kilohertz. Different from BJT, MOSFETs have positive temperatures co-efficient which stop the thermal runaway [1.1].

The introduction of insulated gate bipolar transistor (IGBT) in the 1980s has provided the user with a versatile device. This new device combines the MOS input and the bipolar output characteristics into one device. It has gained quick acceptance by users in the off-line, high-power motor drives and in the UPS industry. The insulated gate bipolar transistor (IGBT) is a three terminal device consisting of gate, emitter and collector. It combines the low on-state voltage drop characteristics of the BJT with the excellent switching characteristics and high input impedance of the MOSFET. They are available in current and voltage ratings much higher than those found in MOSFETs. To

turn ON the N-channel IGBT the collector must be at a positive with respect to the emitter and a positive gate potential will turn-ON the device. The removal of this positive voltage would turn-OFF the device. The IGBT is a one of several options for designers to choose for the power control in switching applications. The features of the IGBT such as high voltage capability, low on resistance, ease of drive and relatively fast switching speeds makes it a technology of choice for moderate speed, high voltage applications [1.1].

A majority of the semiconductor devices are made of silicon. Silicon carbide, are however, under development to the prototype device. Since the introduction of modern power switches such an IGBT, diodes in many applications are subjected to higher voltage and current levels, and are required to switch at higher speed and frequencies [1.6].

1.3 Power Converters

Power electronics converters are a family of electrical circuits which convert electrical energy from one level of voltage, current or frequency to another level, using semiconductor - based electronic switch. Versatile circuit topologies can be found in power electronics for difference applications [1.2].

The essential characteristic of these types of circuits is that the switches are operated only in one of two states – either fully ON or fully OFF, unlike other types of electrical circuits where the control elements are operated in a liner or nearly linear active region. As the power electronics industry has developed, various families of power electronic

converters have evolved, often linked by power level, switching devices, and topological origins [1.2].

In terms of conversion form, four categories can be identified. This conversion can be done by following four ways [1.3]:

(i) AC to AC Converter (cycloconverter)

AC to AC converter controls the rms values of AC voltage applied to the load. This converter converts an AC voltage, such as the mains supply, to another AC voltage. The amplitude and frequency of input voltage tend to be fixed values, whereas both the amplitude and frequency of output voltage tend to be variable without any intermediate conversion link. Single-phase and three-phase cycloconverter is the example of this type of converter. These cycloconverters have a high power output, of the order a few megawatts [1.1].

(ii) AC to DC Converter (rectifier)

AC to DC converter or rectifier changes an alternating voltage to dc voltage. It can classify as uncontrolled and controlled rectifiers. Uncontrolled rectifier circuits are built with diodes, and fully-controlled circuits are built with SCRs, thyristors or power transistors. In AC to DC converter, line frequency diode rectifiers that are increasingly being used to convert the line frequency AC input to an uncontrolled DC output voltage. Primary voltage of a switched mode power supply is the example of uncontrolled DC voltage supply [1.4].

In some application, it is necessary to control the DC voltage. In this case, average DC side voltage can be controlled from a positive maximum to negative maximum. Battery chargers and controlled motor drives use the controlled DC voltage [1.3].

(iii) DC to AC Converter (inverter)

The converter that changes a DC voltage to an alternating voltage is called an inverter. Switch mode DC to AC converter, accepts the DC voltage as the input, and produces the desired AC voltage at the output, whose magnitude and frequency both can be controlled [1.1].

Earlier inverters were built with SCRs. Since the circuitry required turning the SCR off tends to be complex, other power semiconductor devices such as bipolar junction transistors (BJTs), power MOSFETs, insulated gate bipolar transistors (IGBTs) and MOS-controlled thyristors (MCTs) are used nowadays. Currently, only the inverters with a high power rating, such as 500kW or higher, are likely to be built with either SCRs or gate turn-off thyristors (GTOs). There are many inverter circuits and the techniques for controlling an inverter vary in complexity. AC motor drives control and uninterruptible AC power supply widely use this DC to AC converter [1.1].

(iv) DC to DC Converter (chopper)

DC to DC converter circuit was called a chopper. This converter converts a DC supply to another magnitude DC supply. Switched mode DC to DC converters are used to convert the unregulated DC input to a controlled DC output at desired voltage level [1.3].

Nowadays, an SCR is rarely used in DC to DC converter. A power BJT or power MOSFET is normally used in such a converter and this converter is called a switch-mode power supply. The DC to DC converters are widely used in regulated switch-mode DC power supplies and DC motor drive application [1.3].

Buck (step-down) converter, Boost (step-up) converter, Buck-Boost (step-down / step-up) converter, Flyback converter, Forward converter, and Cuk converter are the example of DC to DC converter [1.3].

1.4 Pulse Width Modulation

Pulse Width Modulation (PWM) is employed in a wide variety of application ranging from measurement and communications to power control and conversion. The power semiconductor devices are rapidly gated ON and OFF to vary the effective voltage according to the output frequency. PWM method has been used to describe these kinds of switching pulses. By adjusting the duty cycle of the signal which is modulating the width of the pulse, the average power can be varied. There have been a number of clear trends in the development of PWM concepts and strategies since the 1970s, addressing the main objectives of reduced harmonic distortion and increased output magnitudes for a given switching frequency and the development of modulation strategies to suit different converter topologies [1.2].

This dissertation discussed only the Sinusoidal Pulse Width Modulation (SPWM) and the detail explanation of generating SPWM will be discussed later in the chapter 3.

1.5 Harmonics

Harmonics are defined as the sinusoidal components of non-linear periodic waveform with a frequency that is a whole multiple of the fundamental frequency. These are not apparent but they are harmful. Then we can define the non-linear device as a device that does not draw a sinusoidal current when the sinusoidal voltage is applied [1.8].

All power electronics converters (including those used to protect critical loads) can add to the inherent power line disturbances by distorting the utility waveform due to harmonic currents injected into the utility grid and by producing (EMI). The major harmonic polluters of power system are Diode-rectifiers and Phase-controlled thyristor converters. Due to these harmonics a lot of problem can be found as an increase of overheating of devices, high neutral current, capacitor or transformer failure, power factor reduction, telephone interference, and an increase of power losses [1.8].

The non-fundamental components are called “harmonic distortion”. Harmonic distortion can be classified as voltage harmonic distortion and current harmonic distortion. The amount of distortion in voltage or current waveform is quantified by means of an index called the total harmonic distortion (THD). The THD in the voltage is defined as [1.8]:

$$THD_V = \frac{\sqrt{\sum_{n=2}^{\infty} (V_{n,RMS})^2}}{V_{1,RMS}} \text{----- (1.1)}$$

Where:

n is the number of harmonics.

Current THD can be obtained by replacing the harmonic voltage with harmonic current.

$$THD_i = \frac{\sqrt{\sum_{n=2}^{\infty} (I_{n,RMS})^2}}{I_{1,RMS}} \quad \text{-----} \quad (1.2)$$

Where;

$$I_n = \frac{V_n}{Z_n}$$

Z_n is the impedance at harmonic frequency.

1.6 Objectives of the project

In recent years, much more attention has been paid to find a digitally, flexibility and more efficient solution for controlling the switching devices of converters. The main objectives of this work are:

- (i) To design a single phase inverter that converts DC voltage into AC voltage.
- (ii) To create the schematic of PWM circuit and inverter circuit by using the Orcad simulation.
- (iii) To use the Altera Maxplus II development tool with University Program (UP) board as a programmable logic device to produce a digital PWM signal with frequency 18 kHz to control the power switches in the hardware.
- (iv) Lastly, implement the proposed design into hardware.

1.7 Organization of the dissertation

The dissertation is divided into 8 chapters, including this chapter and organized as follows;

Chapter 2 discusses the literature review of the project.

Chapter 3 discusses the Pulse Width Modulation (PWM) and the different types of PWM switching scheme commonly used in power converter.

Chapter 4 briefly discusses the programmable logic device.

Chapter 5 discusses the development of digital PWM. The Altera MaxPlus II software will be used to generate the digital PWM and will be discussed in details. The Altera simulation results are included.

Chapter 6 presents the implementation of hardware.

Chapter 7 presents the results of OrCad simulation, Altera Maxplus II simulation, and hardware implementation of single phase inverter.

Chapter 8 is the conclusion of the research on developing a digital PWM by using Maxplus II for single phase inverter. Some suggestion on further research works related to the main project will also be presented.

References;

- [1.1] M. H. Rashid, "Power electronic-circuit, devices, and applications," 2nd. Edition Prentice Hall International Edition.
- [1.2] D. Grahame Holmes and Thomas A. Lipo, "Pulse Width Modulation for Power Converters Principle and Practice", Wiley-IEEE Press, October 2003.
- [1.3] Ned Mohan, Tore M. Undeland, and William P. Robbins, "Power Electronics, Converters, Applications, and Design", 3rd edition John Wiley, 2003.
- [1.4] N. A. Rahim, "Advanced power electronics", note under Master Program Dept. of Electrical Eng. University of Malaya, June 2004.
- [1.5] Mariun N., Aris I., Khan N., and Shaheen G.M.A., "Development of power semiconductor devices database for knowledge base system", TENCON 2000, Proceedings, Volume 1, 24-27 Sept. 2000, Page(s):458 - 465 vol.1.
- [1.6] Hefner A., Berning D., McNutt T., Mantooth A., Lai J., and Singh R., "Characterization and modeling of silicon-carbide power devices", Semiconductor Device Research Symposium, 2001 International, 5-7 Dec. 2001 Page(s):568 – 571.
- [1.7] Wilson, T.G., "The evolution of power electronics", Power Electronics, IEEE Transactions on Volume 15, Issue 3, May 2000 Page(s):439 – 446.

- [1.8] A. H. Chowdhury, W.M. Grady, A. Manssor, M. J. Samotyi. "An investigation of Harmonics attenuation and diversity among distributed single-phase power electronic load," in Proc. IEEE Power Engineering Society, Transmission and Distribution 1994 Conf., pp.110-116, April.1994.



PTTA UTHM
PERPUSTAKAAN TUNKU TUN AMINAH