STUDY AND ANALYSIS OF SURFACE CHARGE COLLECTION AND EMISSION SPECTRUM OF PLASMA ASHING PROCESS

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A thesis submitted in
fulfillment of the requirement for the award of the
Degree of Master of Electrical Engineering

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PERPUSTAKAAN TUNKU TUN AMINAH

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Specially dedicated to my beloved mother, Mrs Vejayaletchemee Sivapathy; my soulmate, Dr. Logeetha Kanthaan, father, siblings and friends who have encouraged, guided and inspired me throughout my journey of education…

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ABSTRACT

This research presents an important observation on the total surface charge collection using quantox wafers through the measurement of surface voltage (V_s) on wafer surface with contactless Kelvin Probe for changes in parameters during plasma ashing. In this report, it is covered on the plasma characteristics and performance using the optical emission spectroscopy (OES) measurement, the study on how processing condition change can impact the total surface charge collection and also the uniformity of the charges on the wafer surface, using the quantox measurement. In this study, 3 different types of ashers are tested with varying 5 processing parameters, which where the process time, pressure, gas flow, power and temperature. It is seen that changes in the condition of these parameters do impact on the total surface charge collection and also the uniformity of the charges on the wafer surface. Based on the processing conditions, it is observed that Inductively-Coupled Plasma (ICP) asher model is better in terms of total surface charge collection and uniformity compared to Barrel asher model, which has lower total charge collection but with higher non-uniformity due to the machine chamber configuration. On the other hand, Helical Resonator Plasma (HRP) asher model contributes to higher total surface charge collection with the lesser uniformity, which could potential contribute to plasma induced damage (PID).

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ABSTRAK

Kajian ini membentangkan pemerhatian penting terhadap pengumpulan caj permukaan menggunakan wafer *quantox* menerusi pengukuran voltan *(Vs)* pada permukaan wafer dengan pengukuran tanpa kontak *Kelvin Probe* terhadap perubahan parameter semasa proses *plasma ashing*. Laporan in merangkumi ciri-ciri dan pretasi plasma menggunakan spektroskopi emisi optik (OES), kajian mengenai bagaimana perubahan parameter semasa proses memberi kesan kepada pengumpulan caj keseluruhan dan keseragaman caj pada permukaan wafer, dengan menggunakan pengukuran *quantox*. Dalam kajian ini, 3 jenis *plasma ashers* diuji dengan mengubahkan kondisi 5 parameter proses, iaitu masa proses, tekanan, aliran gas, kuasa dan suhu. Adalah dilihat bahawa perubahan pada parameter ini memberi impak kepada pengumpulan caj keseluruhan dan keseragaman caj pada permukaan wafer. Berdasarkan parameter pemprosesan, diperhatikan bahawa model *asher Inductively-Coupled Plasma (ICP)* adalah lebih baik dari segi jumlah kutipan caj permukaan dan keseragaman caj berbanding dengan model *asher Barrel*, yang mempunyai jumlah pengumpulan caj yang lebih rendah tetapi keseragaman yang rendah yang disebabkan oleh konfigurasi ruang mesin tersebut. Sebaliknya, model *asher Helical Resonator Plasma (HRP)* menyumbang kepada kutipan caj permukaan yang lebih tinggi dengan keseragaman yang lebih rendah, yang boleh menyumbang kepada kerosakan akibat plasma (PID).

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LIST OF SYMBOLS & ABBREVIATIONS

Å - Angstroms
AC - Ashing process chamber
AR - Antenna ratio
Ashrate - Ashing rate; photoresist removal rate over time
BOX - Buried oxide
C - Carbon
°C - Degree Celcius
CD - Critical Dimension
CF_4 - Tetrafluoromethane gas
CH_4 - Tetrafluoromethane gas
CMOS - Complementary Metal Oxide Semicondutor
CO - Carbon monoxide gas
CO_2 - Carbon dioxide gas
CVD - Chemical Vapour Deposition
C_xH_y - Hydrocarbon; commonly known as photoresist
DoE - Design of Experiment
EC - Etching process chamber
EPD - Endpoint Detector
H_2 - Hydrogen gas
H_2N_2 - Forming gas
H_2O - Water
H_2O_2 - Hydrogen peroxide
H_2SO_4 - Sulfuric acid
HRP - Helical resonator plasma
IC - Integrated circuit
ICP - Inductively-coupled plasma

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min	-	minute (time)
MOSFET	-	Metal Oxide Semiconductor Field Effect Transistor
N_2	-	Nitrogen gas
N_2O	-	Nitrus oxide gas
NIST	-	National Institute Standard and Technology
nm	-	nanometer
NMOS	-	Negative-channel Metal Oxide Semiconductor
O_2	-	Oxygen gas
OES	-	Optical emission spectrocopy
OH	-	Hydroxide
PID	-	Plasma induced damage
PMOS	-	Positive-channel Metal Oxide Semiconductor
PR	-	Photoresist
PRS	-	Plasma resist strip
RF	-	Radio frequency
RF	-	Radio Frequency
sccm	-	Standard cubic centimeter per minute
SF_6	-	Sulfur hexafluoride gas
SiO_2	-	Silicon dioxide
SOI	-	Silicon On Insulator
Torr	-	a unit of pressure equals to 133.32 pascals
UTHM	-	Universiti Tun Hussein Onn Malaysia
UV	-	Ultraviolet
V_{kp}	-	Kelvin probe voltage
VLSI	-	Very Large Scale Integration
V_s	-	Surface voltage
W	-	Watt
WRS	-	Wet resist strip

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CHAPTER 1

INTRODUCTION

1.1 Research background

In the last 50 years, development in the semiconductor industry has rapidly grown and newer technology has been introduced to the vast market for faster and efficient usage. Ranging from the technology of 10 um to the currently developed 7 nm, plasma induced damage (PID) is something that cannot be measured in-line on production wafers and can only be tested during the electrical test and functional test. Many studies were carried out and many solution were implemented, such as implementation of antenna rules, protection diodes and connection of antenna to the drain, in order to eliminate PID. However, PID cannot be eliminated fully as improvement of devices occured and some newer device technology such as silicon on insulator (SOI) have serious PID threat. Various kinds of plasma processes in the industry such as deposition, implantation, oxidation and dry etching, are known for their risk of causing PID, which can be defined as the damage caused by the charges in the plasma that get trapped in the thin gate oxide and change the transistor characteristics [1 – 6]. This eventually impacts the electrical and functional characteristics, as well as the reliability of the device [1 – 5], [7]. Plasma ashing; also known as plasma resist strip (PRS) process is known to have a high risk of contributing to PID. PRS is a process of removing the photoresist (PR) with residues and polymers formed at the post-etching or post-implant step [8 – 10]. The PID caused by the plasma ashing process is generally random. The common causes for PID are due to the electron charges present in the plasma and the continuous ion

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bombardment on the surface of the wafer during processing [1 – 2], [9 – 10]. Many types of PRS tools and conditions are present in the industry, which exhibit different impact during the process. Generally, there are single wafer processing PRS and batch processing PRS. Different conditions are utilized for different post-process steps, as it will impact the defect density as well as the performance of the device [11 – 13]. The commonly used method of measuring PID is through the measurement of the surface voltage (V_s) on the wafer surface using a contactless Kelvin Probe [14]. A V_s is usually the result of surface or insulator charge or work function difference and it is most commonly detected with a non-contacting probe, Kelvin Probe [14]. The aim of this study is to understand the plasma characteristics and conditions that will increase the risk of PID on the devices across the different asher models that are commonly used in the industry.

1.2 Problem Statement

Plasma ashing process is a conventional and preferred method of removing the photoresist from wafer surface since it leaves minimal by-product wastes compared to the WRS. On the other hand, plasma ashing process known to contribute to PID on devices, which is not measurable on the product and can be detected only during the electrical or functional test of the device. PID has a major impact on the reliability of the device, causing sudden or early device failure. This has always been a serious threat to the wafer fabrication industry. Although many studies were carried out and many solution were implemented, such as implementation of antenna rules, protection diodes and connection of antenna to the drain, but newer technology such as the SOI has the PID risk.

1.3 Objectives

The objectives of this project are:

i. To characterize the mechanisms of plasma ashing and types of plasma asher models used in the industry.
ii. To characterize and analyze the emission spectral line during O_2 plasma ashing for different plasma ashers.
iii. To investigate and analyze the V_s collection on wafer surface.

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1.4 Scope of Research

In order to meet the above objectives, the project is executed as below:

i. Determination of the plasma conditions using the Ocean Optics HR4000 by collecting the optical emission of O and C in a plasma asher system and evaluate the relative wavelength of the spectrum for each ion species spectral line based on NIST database.
ii. Execution of quantox measurement in order to understand and analyze the total charges collection on the wafer surface and its correlation with PID using the quantox wafers.
iii. Three models of plasma asher system; ICP, HRP and Barrel plasma ashers are used and compared, for better understanding on the comparison between single wafer processing and batch processing models.
iv. Five process parameters; process time, pressure, gas flow, power and temperature, are varied to examine their effects on the total charges collection.
v. Fresh chamber after chamber cleaning is used and the effects of chamber condition are not covered in this study.

1.5 Thesis Outline

This research thesis consists of five chapters. First chapter describes on the overview of this research. The second chapter explains on the literature review of previous studies on this topic. The third chapter provides an elaborate explanation on the methodology used to obtain the results and the equipment, experiment and tools used for data collection. The fourth chapter delves into the analysis and discussion of the results obtained. Final chapter encompasses the summary of findings and the conclusion of the entire research.

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CHAPTER 2

LITERATURE REVIEW

2.1 Fundamental of plasma ashing process

Plasma ashing is a technique used to strip photoresist (hydrocarbon), polymer and residues from a substrate for post-etch, post-implant or rework steps [8]. Normally, O_2 gas is used as the reactant gas. In some applications, N_2 and H_2 forming gases are used to remove hardened polymer and resist from the substrate. The use of O_2 gas for plasma ashing is conventional and is used for all types of asher models [10]. Based on the chemical equation in (2.1), no formation of solid by-product seen during the ashing process, thus it is known to be a cleaner way of removing resist, compared to wet resist strip, which will be explained later in this chapter [12 – 13].

Formation of H_2O is seen in ashing process. Since the processing condition of plasma ashing is typically more than 100 ^oC, no liquid by-product is formed and H_2O is pumped out as steam. Figure 2.1 and Figure 2.2 show the illustration of plasma ashing during and after process. During the plasma ashing process, O_2 gas are ionized and it reacts with the resist only, as it is highly selective towards photoresist/hydrocarbon and will not react with other substrate. Figure 2.2 shows the formation of by-products; consist of carbon dioxide (CO_2), carbon monoxide (CO) and water (H_2O) which will in gaseous state.

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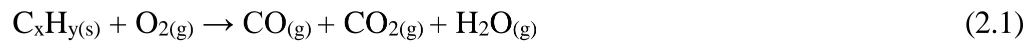
Typical resist strip equation:



$$C_xH_{y(s)} + O_{2(g)} \rightarrow CO_{(g)} + CO_{2(g)} + H_2O_{(g)} \quad (2.1)$$

Chemical Equation 2.1 shows that hydrocarbon (PR) reacts with oxygen and forms carbon monoxide, carbon dioxide and water steam.

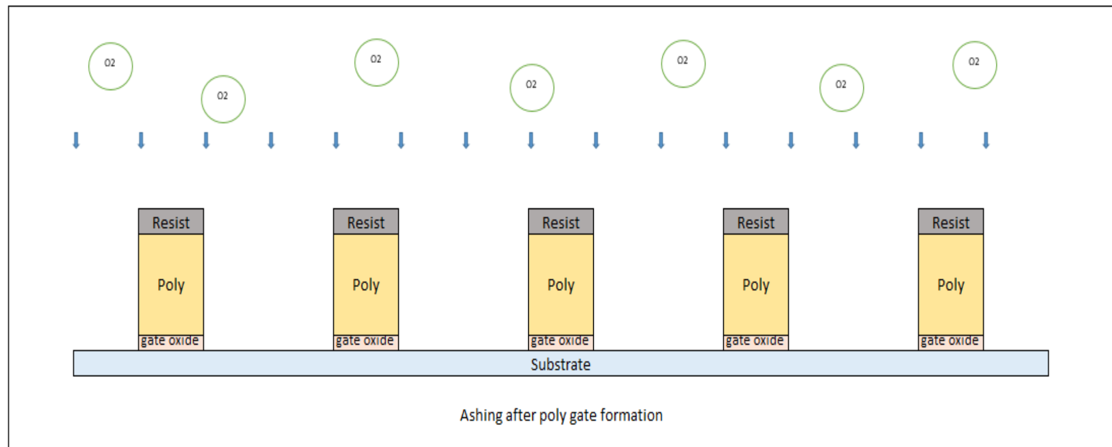


Figure 2.1 : During process - O_2 plasma ashing

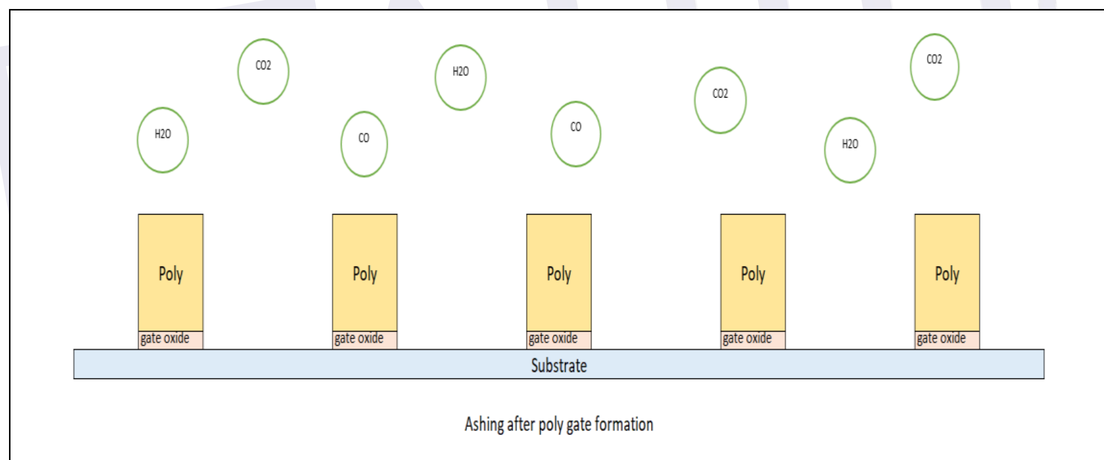
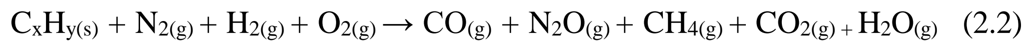


Figure 2.2 : After process - O_2 plasma ashing

Besides typical O_2 plasma ashing, forming gases are added (96% of N_2 + 4% of H_2) with O_2 gas, in order to remove hardened polymer and resist from the substrate. The gas ratio of H_2N_2 gases used with O_2 is generally 1:10. This is because forming gas is used to react with hardened polymers, formed at selected post implant steps that are chemically inert with O_2 gas. When forming gas is included, it will increase O radicals for the process, thus this increases the ashing rate performance [15 – 16]. Using conventional plasma ashers, the forming gas is used to react with the hardened crust of the resist; side effect of high dose and energy implants.

Resist strip with forming gas equation:



$$C_xH_{y(s)} + N_{2(g)} + H_{2(g)} + O_{2(g)} \rightarrow CO_{(g)} + N_2O_{(g)} + CH_{4(g)} + CO_{2(g)} + H_2O_{(g)} \quad (2.2)$$

Chemical Equation 2.2 shows that hydrocarbon (PR) reacts with oxygen and forming gas that forms carbon monoxide, nitrous oxide, methane, carbon dioxide and water steam.

Besides forming gas, gases like SF_6 or CF_4 can be incorporated during the plasma ashing process. These impurity gases help to improve the plasma ashing rate, result of an increased level of oxygen radicals in the plasma chamber and initiation rate of hydrogen from the photoresist by the fluorine ions presented in the plasma [5]. However, this is not preferred since SF_6 and CF_4 will react with the substrate, resulting in substrate loss due to its reaction and reaction with the metal substrate, eventually causing corrosion of the metal structure.

After typical post etch or post implant steps, plasma ashers are used as the main stripper tool. For some steps, the wet resist strip (WRS) is inserted after plasma ashing step, in order to remove remaining polymers or residues that were unable to be removed by the typical plasma ashing process [5]. Illustration of remaining polymers or residues on the substrate is as in Figure 2.3. Figure 2.4 shows the typical process flow involving WRS after PRS.

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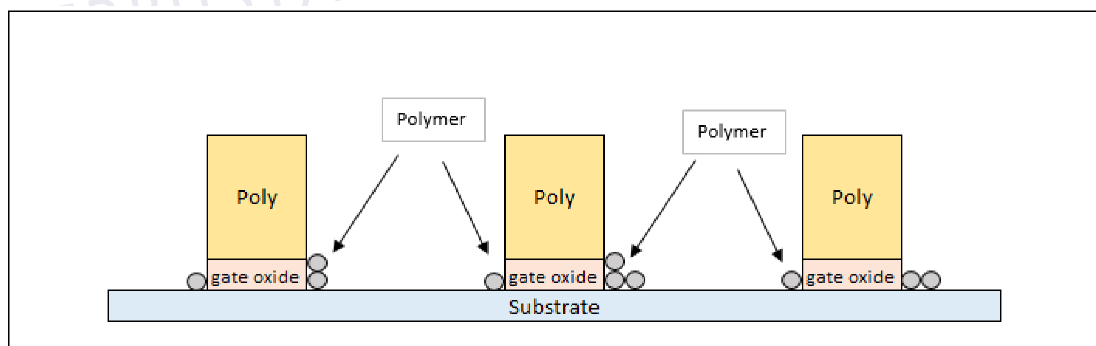

Figure 2.3 : Unremoved polymer after PRS

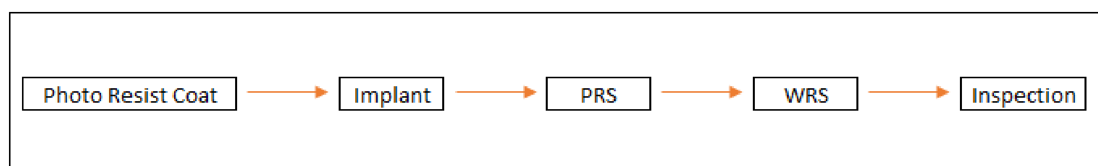

Figure 2.4 : Process flow with WRS

The WRS can be used directly for resist stripping since the chemicals used are sulfuric acid (H_2SO_4) and hydrogen peroxide (H_2O_2). However, there are many disadvantages of WRS. As it involves strong acids and hydrocarbon solvents that are expensive, these are highly toxic and harmful for humans and environment. As not being environmental friendly, the large volume of liquid reactants and by-products need to be handle with precaution. Meanwhile, the impact on the process will be that, under normal circumstances, the processing time is longer compared to PRS, which impacts on the throughput or wafers process per hour that affects the manufacturing productivity.

On the other hand, under certain circumstances, when the WRS bath is saturated with by-products, the stripping efficiently will be greatly affected and this will lead to photoresist/polymer residues on the substrates. This will definitely impacts the device performance [5], [17]. Besides, direct usage of WRS without plasma ashers will generate vast waste since the bath used for the WRS need to be changed frequently due to by-product formation. Meanwhile, for the PRS, the general advantages are that it is non-toxic and non-corrosive, easier to control and cheaper for the industrial applications [5], [17].

In general, there are two types of plasma ashers used. Firstly, there is the in-situ plasma ashing process. For etching tool groups, such as poly or metal etchers, the etching condition occurs at the etching chamber (EC) and the tool has an ashing chamber (AC). The usage of in-situ AC is to aid in the removal of the thin photoresist and polymer, in the case of the poly etching process. For the metal etching process, the in-situ AC is to remove the photoresist as well as provide passivation for the metal structure, in order to prevent metal structure corrosion.

Secondly, we have the ex-situ plasma ashing process. Ex-situ plasma ashing typically used for post-implant or post wet etch process. It can also be used for thick resist removal and rework of photoresist, such as in cases where the photolithography specification is out of control, whereby it requires the resist to be removed and recoated.

For both types of plasma ashers, no potential bias is applied on the subsrate, which is necessary to attract ions. This is due to the tool configuration of random substrate surface O_2 ion bombardment that is crucial in avoiding substrate loss due to oxidation as plasma ashing process uses high gas flow of O_2 [18]. On the other hand, this random ions bombardment on wafer surface creates a non-uniformed ions

PTTA UTHM PERPUSTAKAAN TUNKU TUN AMINAH

REFERENCES

1 Francis F.Chen, “Plasma-Induced Oxide Damage: A Status Report.”, ppg-1566, October, 1996

2 Hyungcheol Shin, Chih-chieh King, Chenming Hu, “Thin Oxide Damage by Plasma Etching and Ashing Processes”, proc. IRPS, 1992

3 H C Shin, Chenming Hu, “Thin Gate Oxide Damage due to Plasma Processing”, Semicond. Sci. Technol. 11, 1996

4 Chao-Hsin Chien, Chun-Yen Chang, Horng-Chih Lin, Tsai-Fu Chang, Shean Guang Chiou, Liang-Po Chen and Tiao-Yuan Huang, “Resist-Related Damage on Ultrathin Gate Oxide During Plasma Ashing”, IEEE Electron Device Letters, Vol 18, No 2, Feb 1997

5 Sychyi Fang and James P. McVittie, Senior Member, IEEE, “Thin-Oxide Damage from Gate Charging during Plasma Processing”, IEEE Electron Device Letters, Vol 13, No 5, May 1992

6 K. P. Cheung, “Mechanism of Charging Damage during Plasma-Enhanced Silicon Nitride/Oxide Thin-Film Deposition”, IEEE Transactions on Device and Materials Reliability, 2006

7 A. Martin, C. Bukethal, K. H. Ryden, S. Baier and M. Schwerd, “Quantitative Reliability Assessment of Plasma Induced Damage on Product Wafers with WLR measurements”, IEEE International Integrated Reliability Workshop Final Report, 2008

8 QY Han, “Plasma Ashing Process”, US Patent 6,548,416, Apr. 2003

9 Fujimura et al, “Method of Removing Photoresist on a Semiconductor Wafer”, US Patent 4,938,839, Jul. 3, 1990

10 Sami Franssila, “Introduction to Microfabrication”, Second Edition, Wiley Publication, 2010

11 Mao-Sung Tseng, “Low Temperature Dry Process for Stripping Photoresist after High Dose Implantation”, US Patent 5,811,358, Sep. 1998

PTTA UTHM PERPUSTAKAAN TUNKU TUN AMINAH

12 J S Hallock, "Process for Removal of Photoresist after Post Ion Implantation", US Patent 6,524,936, Feb 2003

13 Masashi Kikuchi, "Plasma Ashing Method and Apparatus Therefor", US Patent 5,226,056, Jul 1993

14 Dieter K Schroder, "Surface Voltage and Surface Photovoltage: history, theory and applications", Measurement Science and Technology 12(3):R16, Feb. 2001

15 S. Nawaz, H. S. Rhee, D. H. Kim and S. J. Yoon, "Experimental measurement of E and H mode transition in O2, N2 and O2-N2 gases in cylindrical ICP source for photoresist dry-strip applications" IEEE International Conference on Plasma Science (ICOPS), 2017

16 S. Mahesh, X. Bin, M. F. Karim, H. Odd and Z. Xu, "Improvement of Gate Oxide Reliability with O2 Gas Ash Process in Post Poly Resist Strip and Spacer Etch Asher Process in 45mm CMOS Technology", IEEE Proceedings of 16th IPFA, 2009

17 Michael Quirk, Julian Serda, "Semiconductor Manufacturing Technology", Chapter 16 Etch, Prentice Hall, 2001

18 B. W. Chan, B. C. Perng, L. Sheu, Y. H. Chiu, and H. J. Tao, "Plasma Induced Substrate Damage in High Dose Implant Resist Strip Process", IEEE 8th International Symposium Plasma- and Process-Induced Damage, 2003

19 Y. Sacchettini, J. P. Carrere, V. Goiffon and P. Magnan, "Plasma Antenna Charging in CMOS Image Sensors", IEEE International Reliability Physics Symposium (IRPS), 2019

20 Roger Patrick, Frank Bose, Philippe Schoenborn, Harry Toda, "Coil Configurations for Improved Uniformity in Inductively Couple Plasma Systems", US Patent 5,401,350, March 28, 1995

21 V. Nagorny, V. Vaniapura and V. Surla, "Validation of High Efficiency ICP Source Performance for Advanced Resist Ashing", 26th Annual SEMI Advanced Semiconductor Manufacturing Conference (ASMC), 2015

22 Daniel L. Flamm, "Processes depending on Plasma Generation using a Helical Resonator", US Patent 5,304,282, Apr. 19, 1994

23 Raymond Joe, "Batch Photoresist Dry Strip and Ash System and Process", US Patent US 7,387.968 B2, Jun 17, 2008

24 G.Cellere, M.G.Valentini and A.Paccagnella, "Correlation between Soft

PTTA UTHM PERPUSTAKAAN TUNKU TUN AMINAH

Breakdown and Plasma Process Induced Damage", 6th International Symposium on Plasma- and Process-Induced Damage, 2001

25 K. Eriguchi, M. Kamei, K. Okada, H. Ohta and K. Ono, "Threshold Voltage Shift Instability Induced by Plasma Charging Damage in MOSFETs with High-κ Dielectric", IEEE International Conference on Integrated Circuit Design and Technology and Tutorial, 2008

26 D. Zhu, S. K. Loh, S. P. Neo and G. B. Ang, " Failure Analysis Methodology for Gate Oxide Breakdown Induced by PID", 17th IEEE International Symposium on the Physical and Failure Analysis of Integrated Circuits, 2010

27 F. L. Chow and A. Chin, "Failure Analysis on Plasma Charging Induced Damage due to Effect of Circuit Layout & Device Structure Marginality", 19th IEEE International Symposium on the Physical and Failure Analysis of Integrated Circuits, 2012

28 C. Chang, J. Zhou, C. Ni, O. Chan, S. Sun, W. Suen, S. Mings, M. Bevan, P. M. Liu, P. Hsieh, C. Chang and R. Hung, "The Effect of Interfacial Oxide and High- κ Thickness on NMOS Vth Shift from Plasma-Induced Damage", IEEE Silicon Nanoelectronics Workshop (SNW), 2014

29 G. Hiblot and G. V. D. Plas, "Full-Loop Equivalent Circuit Model for Plasma-Induced Damage Simulation", IEEE Transactions on Plasma Science, 2018

30 G. Hiblot, Y. Liu, G. Hellings and G. V. D. Plas, "Comparative Analysis of the Degradation Mechanism in Logic and I/O FinFET Devices Induced by Plasma Damage", IEEE International Reliability Physics Symposium (IRPS), 2019

31 L. Cheng, X. Xu, C. J. Wu, J. V. Chang, "Investigation of BEOL Plasma Process Induced Damage Effect on Gate Oxide", IEEE Annual Reliability and Maintainability Symposium (RAMS), 2015

32 W. H. Choi, S. Satapathy, J. Keane, C. H. Kim, "A Test Circuit based on a Ring Oscillator Array for Statistical Characterization of Plasma-Induced Damage", Proceedings of the IEEE 2014 Custom Integrated Circuits Conference, 2014

33 Z. Wang, J. Ackaert, A. Scarpa, C. Salm, F. G. Kuper and M. Vugts, "Strategies to cope with Plasma Charging Damage in Designand Layout Phases", IEEE International Conference on Integrated Circuit Design and Technology ICICDT, 2005

PTTA UTHM PERPUSTAKAAN TUNKU TUN AMINAH

34 M. Akbal, G.Ribes, M.Guillerment and L. Vallier, "Plasma Induced Damage Investigation in the Full Depleted SOI Technology", IEEE International Conference on IC Design & Technology (ICICDT), 2015

35 M. Akbal, G. Ribes, T. Poiroux, J-P. Carrere and L. Vallier, "New Circuit Model for Investigating Plasma Damage in FDSOI Devices", IEEE International Reliability Physics Symposium, 2014

36 M. Akbal, G. Ribes, W. Arfaoui and L. Vallier, "Plasma Process Inducing Gate Oxide Breakdown in the FDSOI Technology", IEEE International Integrated Reliability Workshop Final Report (IIRW), 2014

37 X. Duan, W. K. Chien, D. Zhang, S. Yu, D. Zhao, J. Niu, "Resolving Plasma Induced Damage for a CMOS Embedded-OTP Technology", IEEE China Semiconductor Technology International Conference (CSTIC), 2016

38 K. P. Cheung, "Advanced Plasma and Advanced Gate Dielectric – A Charging Damage Prospective", IEEE Transactions on Device and Materials Reliability, 2007

39 Y. Yamamoto, K. Segi, S. Tsuda, H. Makiyama, T. Hasegawa, K. Maekawa, H. Shinkawata and T. Yamashita, "The study of Plasma Induced Damage on Silicon on Thin BOX", IEEE SOI-3D-Subthreshold Microelectronics Technology Unified Conference (S3S), 2018

40 R. Kishida, A. Oshima and K. Kobayashi, "Negative Bias Temperature Instability caused by Plasma Induced Damage in 65mm Bulk and Silicon on Thin BOX (SOTB) Processes", IEEE International Reliability Physics Symposium, 2015

41 T. Dao, "Process Induced Damage: What challenges lie ahead?", IEEE 8th International Symposium, 2003

42 A. C. Mocuta, T. B. Hook, A. I. Chou and T. Wagner, "Plasma Charging Damage in SOI Technology", 6th International Symposium on Plasma- and Process-Induced Damage, 2001

43 T. B. Hook, "Charging Damage and SOI", International Conference on Integrated Circuit Design and Technology, 2005

44 W. Lai, D. Harmon, T. Hook, V. Ontalus and J. Gambino, "Ultra-Thin Gate Dielectric Plasma Charging Damage in SOI Technology", IEEE International Reliability Physics Symposium Proceedings, 2006

45 Barry P. Linder abd Nathan W. Cheung, "Calculating Plasma Damage as a

PTTA UTHM
PERPUSTAKAAN TUNKU TUN AMINAH

Function of Gate Oxide Thickness", 3rd International Symposium on Plasma Process-Induced Damage, 1998

46 Tomasz Brozek and Thuy Dao, "Effect of Device Type and Plasma Process on the Oxide Thickness Dependence of Plasma-Induced Charging Damage", 3rd International Symposium on Plasma Process-Induced Damage, 1998

47 Chao-Hsin Chien, Chun-Yen Chang, Horng-Chih Lin, Tsai-Fu Chang, Shean Guang Chiou, Szu-Kang Hsien and Tiao-Yuan Huang, "The Role of Resist for Ultrathin Gate Oxide Degradation during O_2 Plasma Ashing", IEEE Electron Device Letters, Vol 18, No 5, May 1997

48 J. Mercier, T. Dao, H. Flencher, B. Jean, D. B. Oscar and P. K Aum, "Process Induced Damages from Various Integrated Circuit Interconnection Designs – Limitations of Antenna Rule under Practical Integrated Circuit Layout Practice", IEEE 8th International Symposium Plasma- and Process-Induced Damage, 2003

49 K.P. Cheung, C-T Liu, C-P Chang, J.I. Colonell, W-Y-C Lai, C-Pai, H.Vaidya, R.Liu, J.T.Clemens and E.Hasegawa, "Charging Damage in Thin Gate-Oxides – Better or Worse?", 3rd International Symposium on Plasma Process-Induced Damage, 1998

50 Sychyi Fang, Shigemi Murakawa and James P. McVittie, "Modeling of Oxide Breakdown from Gate Charging during Resist Ashing", IEEE Transactions on Electron Devices, Volume: 41, Issue: 10, Oct 1994

51 S. Ma, M. Dahimene, C. Bjorkman, H. Shan and R. Ramanathan, "Plasma Damage Evaluation of an Integrated In-Situ Directional Resist Stripping Process in Magnetically Enhanced RIE Etcher for Dual Damascene Application", 5th International Symposium on Plasma Process-Induced Damage, 2000

52 S.P.Koirala, M.H Gordon, L.Cai, S.L.Burkett and L.W.Schaper, "Optical Emission Spectroscopy in a Reactive Ion Etching System", IEEE Region 5 Technical Conference, 2007

53 Wang Wei, Bi Junjie, and Zhao Junpeng, "Plasma Etching Process Monitoring with Optical Emission Spectroscopy", International Conference on Industrial Mechatronics and Automation, 2009

54 S. W. Park, G. J. Seong, K. H. Baek, Y. 1. Kim, K. S. Shin, Y. G. Shin and H. G. Kang, "Etch Rate Monitoring with Optical Emission Spectra in Dry

PTTA UTHM
PERPUSTAKAAN TUNKU TUN AMINAH

Etching Process", IEEE International Conference on Plasma Science, 2011

55 Sundeep Rangan, Costas Spanos and Kameshwar Poolla, "Modeling and Filtering of Optical Emission Spectroscopy Data for Plasma Etching Systems", IEEE International Symposium on Semiconductor Manufacturing Conference Proceedings, 1997

56 Pamela P. Ward, "Plasma Process Control with Optical Emission Spectroscopy, Seventeenth IEEE/CPMT International Electronics Manufacturing Technology Symposium. 'Manufacturing Technologies - Present and Future'", 1995

57 Sumaira Nosheen, Shahzad Alam, Muhammad Irfan, M.Usman Ali Qureshi and Salman Ahmad, "Optical Emission Spectrometer, Principle and Latest Industrial Applications, International Journal of Material Science (IJMSCI)", Volume 3 Issue 4, December 2013

58 Ming-Yi Lee, John Hu, Wilbur Catabay, Philippe Schoenborn and Aldone Butkus, "Comparison of CHARM-2 and Surface Potential Measurement to Monitor Plasma Induced Gate Oxide Damage", 4th International Symposium on Plasma Process-Induced Damage, 1999

59 Siti Kudnie Sahari, Jane Chai Hai Sing, Khairuddin Ab. Hamid, "Quantox Analysis of SC-l and SC-2 Variables in Wafer Surface Preparation", IEEE International Conference on Semiconductor Electronics, 2008

60 Sonu Daryanani and Jeff Shields, "Comparison of Implant Charging Results Obtained with QUANTOXs and CHARM-2", IEEE International Conference on Ion Implantation Technology. Proceedings, 1998

PTTA UTHM PERPUSTAKAAN TUNKU TUN AMINAH