# IMPLEMENTATION OF ACR AND AVR CONTROLS FOR HIGH VOLTAGE GAIN DC-DC CONVERTER

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#### **ABSTRACT**

Step up power conversion is universally used in many applications. The application that uses step-up power conversion can be observed in renewable energy such as photovoltaic (PV) system, wind turbine, data center and Electric vehicle. There are many applications which use the DC-DC boost converter to get higher DC voltage from the low input voltage. In this project, Marx topology boost converter (MTBC) analyzed and proposed for conversion from low input dc voltage to high output dc voltage. (MTBC) depends on the principle of the Marx generator. The proposed (MTBC) is multi-stage and consist from 4-stage, by multistage of converter the stress in the components will be reduced, where the parallel charging at input side to reduce the current stress, and series discharging at output to reduce the voltage stress. The stress on the components of the converter will inversely proportional with a number of stages. By implementation of ACR and AVR combination with using PI control technique the output voltage can be controlled. Based on the simulation results the obtained output voltage 400V DC by boosting input voltage 48V DC and by using 4stage proposed converter, but any drop in the value of input voltage will effect on the output voltage, so that when battery voltage drop to 40V the output will be 340V. After implementing the control system for the AVR and ACR and combine between them, it will be possible to obtain 400V from different value input voltage (40V, 45V, 55V), as well as for 450V and 500V output voltage.

#### **ABSTRAK**

Peningkatan kuasa digunakan secara meluas pada banyak aplikasi. Aplikasi yang menggunakan peningkatan kuasa boleh dilihat dalam tenaga boleh diperbaharui seperti sistem photovoltaic (PV), turbin angin, pusat data dan kenderaan elektrik. Terdapat banyak aplikasi yang menggunakan DC-DC boost converter untuk mendapatkan keluaran voltan DC yang lebih tinggi yang disebabkan voltan masukan yang rendah. Dalam projek ini, Marx topology boost converter (MTBC) dianalisis dan dicadangkan untuk penukaran daripada voltan masukan de yang rendah ke keluaran voltan de yang tinggi. MTBC bergantung kepada prinsip penjana Marx. Cadangan MTBC adalah multi-stage dan terdiri daripada 4 stage, di mana tekanan pada komponen dapat dikurangkan, di mana pengecasan selari di sebelah input untuk mengurangkan tekanan arus, dan pelepasan cas secara siri yang dikeluarkan pada output dapat mengurangkan tekanan voltan. Tekanan pada komponen penukar akan berkadar sebanding dengan beberapa peringkat. Dengan pelaksanaan kombinasi ACR dan AVR dengan menggunakan teknik kawalan PI, voltan keluaran dapat dikawal. Berdasarkan hasil simulasi, voltan keluaran 400 V DC dapat dicapai dan voltan masukan 48 V DC di mana menggunakan 4-stage converter yang dicadangkan, namun penurunan nilai voltan masukan akan mempengaruhi voltan keluaran, sehingga apabila voltan bateri jatuh ke 40 V output akan menjadi 340 V. Selepas melaksanakan sistem kawalan untuk AVR dan ACR dan menggabungkan di antara mereka, 400 V dapat diperoleh dari nilai voltan masukan yang berbeza (40V, 45V, 55V), serta voltan keluaran 450V dan 500V.

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#### LIST OF ABBREVIATION

Vout - Output voltage

Vref - Reference voltage

Vin - Input voltage

PWM - Pulse Width Modulation

MOSFET - Metal Oxide Semiconductor Field Effect

CCM - Continuous Conduction Mode

DCM - Discontinuous Conduction Mode

MTBC - Marx Topology Boost Converter

PI - Proportional-Integral

AVR - Automatic Voltage Regulator

ACR - Automatic Current Regulator

CMC - Current Mode Control

VCM - Voltage Mode Control

β - Boost Ratio

D P Duty cycle

S - Switch

ton - Time ON

toff - Time OFF

*L*in - Input inductance

 $\Delta Iin$  - Input current ripple

Lout - Output inductor

Cm - stage capacitance

#### **CHAPTER 1**

#### INTRODUCTION

## 1.1 Background Studies

In contemporary electronic circuit usages, conversion from DC to DC applications plays a significant role. Beginning from domestic applications to industrial control various kinds of direct/indirect DC-DC converters are applied recently. Among those boosts, the converter devices are utilized fundamentally to step up the input voltage at the load end [1]. Boost Type DC-DC converter (BDC) has established varied usages in different types of voltage regulation areas like LED luminaries, laser power source and special mechanical manufacturing. Though, in view of its essential features like non-linearity and unstable zero dynamics, when taking capacitor voltage as output, its performances, particularly in dynamic response, are considerably limited as they are applied in fast voltage or recent tracking purposes [2]. DC-DC boost converters are the main element to interface power sources to the EV's dc-bus. In EVs, fuel cells (FCs) batteries and super-capacitors (SCs) are regularly applied as devices storing energy. Combining such energy sources causes FC-SC-battery hybrid power system (HPS) [3]. Several PV usages need a high-efficiency and high step-up voltage gain DC-DC converter for increasing and regulating the low DC voltage into an appropriate usage voltage. In addition, the inter-connection among the Photovoltaic panels and the load, the DC-DC converter additionally does the important function of boosting the PV system power outcome [4]. Several low voltage high current dc sources, such as an ultra-capacitor bank, NiMH battery and Li-Ion battery need high boost ratio DC-DC converters for transferring

their energy into higher dc systems. Though, it is challenging for designing a universal interface among these kinds of dc sources and higher voltage designs due to the high variation of input voltage and the high current needs [5].

A lot of applications like industrial, need to transform the low voltage to high-level voltage by an apply DC-DC converter. Some of them need (200V-400V) output voltage and the others like telecommunication systems, uninterrupted power supplies that need 380V by boost the 48V input [6][7]. By connecting the conventional DC-DC converters in cascade connection, the high voltage gain is realized, but because of high input current that causes high conduction losses and number of cascading connections, the efficiency will minimize. For a high boost ratio, these circuit configurations are not recommended. a Marx generator is one of the interesting structures which achieves a high boost ratio in the DC-DC converter. The target from the Marx generator is getting high voltage from a low input voltage. The principle of Marx impulse generator that applied in DC-DC converter high voltage gain, where minimization of current stress will be by parallel connection at the input side and voltage stress minimization will be by series connection at the output side [8]. In the proposed circuit the reduction for volume and inductance of the output inductor are obtained by apply interleaved switching scheme [9].

The control methods for DC-DC power converters are essentially categorized into 2 key collections as voltage mode control (VMC) and current mode control (CMC), there are various control methods has been created like digital controllers and sliding mode controller [10]. For keeping the constant output voltage of converter digital PI controller is applied using feedback signal that could be used with a microcontroller, fuzzy logic, FPGA or digital signal processing [11]. For organization DC-DC boost converter at the required output voltage, there are main challenges in implementing a humble output feedback PI controller, well-known in the electromechanical conversion community and power electronics, that is the existence of unstable zero dynamics in the converter model. So, earlier systems that propose classic PI controllers depend on a linear and small signal pattern analysis. These types of controllers are super sensitive to model uncertainties since the alteration of the operating point decrease the small signal model validation. A more stabilized to this issue has been supported by indirect regulation the output voltage through the input

inductor current in a cascaded PI controller's system [12]. Figure 1.1 explains the block diagram of project.

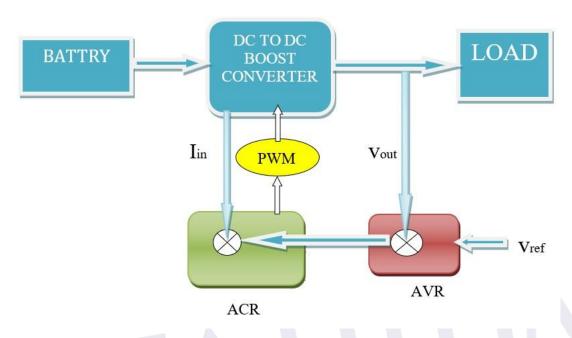


Figure 1.1: Block Diagram of project

## 1.2 Problem Statements

A traditional DC-DC boost converter and the other structures of DC-DC converters in charge of high losses. It suffers from current stress because of the large input current as a result of that high conduction and copper losses occur, and the output voltage stress is very high due to the high output voltage. Some topology is needed to be taken to reduce the losses. Hence, by applying Max topology boost converter (MTBC) its reduction will be possible where a parallel connection is applied at input side in order to reduce the current stress while a series connection is applying in output side to reduce the voltage stress and this lead to a reduction in losses. The undesirable feature of this topology is uncontrolled output voltage. A multi-stage Marx topology converter of this project with implementation AVR at the output side and ACR at the input side with using PI control technique can control the output voltage. as a result, the converter is operated in a good performance and the highly efficient.

# 1.3 Objectives of the Study

- To reduce current stress at input side and voltage stress on switching devices and capacitors by adopting multi-stage dc to dc boost converter structure.
- To control output voltage and input current by implementing of ACR and AVR to achieve balanced input current and constant output voltage.

## 1.4 Scope

To perform the obvious targets, the scope could be presented as follows:

- 1- The multistage is adopted for converter designed and main components selection.
- 2- The switching scheme is identified by utilizing a synchronized switching scheme and the mode operations.
- 3- The concept of control technique is clarified which is a combination of the AVR and ACR with a PI control technique.
- 4- The MTBC 4-stage simulated and tested using MATLAB software Simulink.
- 5- Parameters comparison among the MTBC and conventional DC-DC converter in terms of current at the input side and the voltage across the capacitor (voltage rating) at output side are concerned.
- 6- ACR and AVR have been established and simulated by MATLAB program.
- 7- AVR and ACR combination established and simulated by MATLAB program Simulink and the performance of MTBC have been analyzed.

#### **CHAPTER 2**

#### LITERATURE REVIEW

#### 2.1 Introuction

The DC-DC boost converter could be commonly utilized in applications like EV, telecommunication, DC-DC transmission line. Many structures in past researchers are able to produce high voltage. Each structure has its own features. The important data and information for previous studies are very necessary to build a good literature review for this project.

#### 2.2 DC-DC converter

The power converters become common at present because it utilizes in several areas [13]. The different levels of voltage can be produced by dc to dc converter either high or low voltage level, depending on the kind of application [14]. Implement the inductive and capacitive filter, as a result of High-frequency switching. The other choice by using the transformer to obtain the desired voltage [15]. Figure 2.1 explains the block diagram of the DC-DC converter.

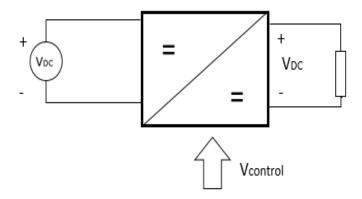


Figure 2.1: The basic of the DC-DC converter block diagram

# 2.3 Applications of high voltage gain DC-DC boost converter

High step-up voltage gain DC-DC converter is suggested to match the needed voltage level [16]. For instance, the operating voltage needed in high-intensity discharge lamps to automobile headlamps [17]. High voltage DC-DC Converters are additionally applied in UPS, industry testing and renewable energy [18]. Moreover, for getting a low THD level and high value of PF in boost PFC systems, it is important for getting a high level of AC voltage [19]. And in X-ray power generator, computer periphery, servo-motor drives they could be applied additionally in control systems, PV designs battery charger units, fuel-cell-based energy conversion [20].

#### 2.4 Conventional DC-DC boost converter

The design of this converter includes input voltage, an inductance, S<sub>n</sub> switch, D diode, and C capacitor that connected with a resistance R in parallel. The good converter should be fast in performance. Examples of switching devices which have faster performance compared with other MOSFET, IGBT, and BJT. The operations of charging and discharging of inductor depend on a switch when being in OFF or ON. If the switch ON the inductor is charging and the diode will be reversed biased. In OFF state of the switch, the diode is in forward biased and the energy stored in inductor transmitted to the output circuit [21].

During charging the inductor become as load, but in discharging will be like a source. the mode operation of DC-DC converter is continuous conduction mode and

discontinuous conduction mode [22]. The unwanted characteristics of this type of DC-DC converter, the high losses due to the high current of input from low voltage and the components of the converter are illustrated in Figure 2.2.

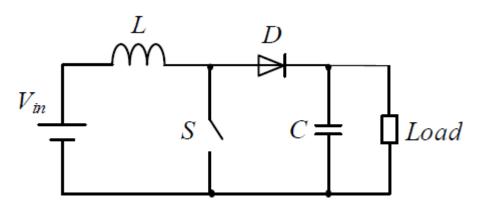


Figure 2.2: Conventional DC-DC boost converter.

# 2.5 Non-isolated and isolated DC-DC converter

There are 2 main groups of DC-DC converter, the isolated DC-DC converter and non-isolated DC-DC converter. Transmitting power from the input part to the output part by the transformer is performed by using an isolated DC-DC. Push-pull converter, forward converter, and fly-back converter consider as examples for the isolated DC-DC converter. Meanwhile, direct power transfer will be possible to apply the non-isolated dc to dc converter [23]. An instance of the non-isolated dc to dc converter that generally applies in applications is a buck-boost converter, buck converters and boost converters [24]. Maximize or minimize transformer input voltage has been done by regulating the turn ratio [25]. The 'isolation' signifies to the barrier of electrical voltage that exists among the output and input of the DC-DC converter. The withstanding of barrier arrives in thousands of volts, depends on kind of standard. The isolated DC-DC converter is bigger and heavier than a non-isolated DC-DC converter [8]. Voltage rising across switch will occur due to the transformer leakage inductance [26].

The non-isolated DC-DC converter directly converts voltage from the DC input to the DC output, it is famous as a conentional DC-DC converter. This kind of converters categorized into two kinds which mostly use in applications [27]. Buck converter is one of these kinds, it transforms from the high level of voltage to the low

level [28]. The low input voltage applications as a battery charger, laptop adapter mostly use buck converter [29]. Meanwhile, the most popular DC-DC convrters is the boost converter, it transforms the voltage from low level to high level, it is famous as the step-up DC-DC converter [30].

## 2.6 Traditional and extra high voltage gain DC-DC converters

Traditional DC-DC (boost) converters are famously used in HV (high voltage) applications, with limited performance because of low efficiency, less output voltage transfer gain, complexity in control. The technique of lift voltage a famous way widely utilized in the design of the electronic circuit and effectively implemented in DC-DC converter applications. Output voltage increases proportionally with step-up stages number. The above limitations have eliminated by design extra high voltage dc to dc converter from classical converter, by implementing additional passive (inductor/ capacitor) components the voltage increase in a simple geometric progression. as a result, enhancement of the voltage gains as per power-law terms. The performance of this DC-DC extra high voltage converter posse advantageous in comparison to classical DC-DC version as follows:

- High voltage transfer ratio gain (k).
- Wide range of control with lower ripple at the output.
- High power density and high efficiency.
- One sensor is needed for Closed-loop compensator [31].

# 2.7 Quadrupler Interleaved Boost Converter

The structure of this converter able to improve low voltage to a higher level [32]. High voltage gain can achieve by applying this structure. The quadrupler interleaved boost converter is an adjustment from two phases interleaved boost converter[33]. Figure 2.3 illustrates the circuit of the converter. Figure 2.3 demonstrates that the design includes 2 inductance, 4 diodes, 2 Metal oxide semiconductor FET as well as 4 capacitors [34]. In quadruple interleaved boost converters have extra 2 capacitor and 2 diodes from 2 phase interleaved boost converter as illustrated in Figure 2.4. However, by this structure high voltage is possible to obtain, to realize the condition

of the continuous current mode, the D value which refers to the duty cycle should be above zero point five. Meanwhile discontinuous current mode (DCM) will happen if the value of D reduces lesser than zero point five, and no energy could be transmitted among inductor, capacitor and side of load in this state [32][34].

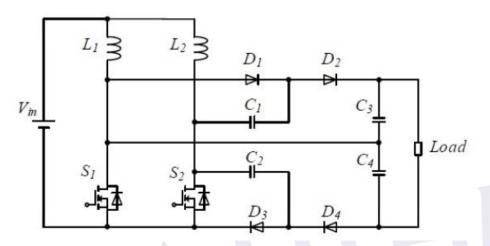


Figure 2.3: Quadrupler boost converter design

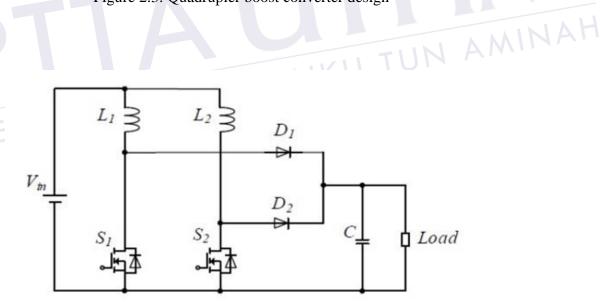


Figure 2.4: Quadrupler boost converter design.

# 2.8 Inductor-less Multi-stage Modlar Capacitor Clamped DC-DC Converter

This converter is an inductor less structure able to produce a high value of voltage gain. Energy transfer between input and output for this structure will be by using capacitors [35]. The output value of voltage is proportional with the converter's stages. One stage or modular block includes a capacitor with 3- switching devices [36]. Figure 2.5 explains single stage. Each stage of IMMCCC is able to produce output voltage value double value of input voltage. The getting of high voltage is possible by using IMMCCC without increase duty cycle value [35]. However, in this structure the output voltage will proportional with a number of stages, to reach the desired voltage should increase the number of stages with a constant duty cycle at 0.5.

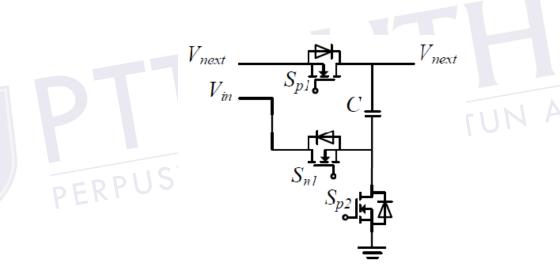


Figure 2.5: Modular structure of the IMMCCC

Number of the cascading stages depends on the desired output value of voltage. Figure 2.6 explains 3 stages of converter. The switching configuration is essential to get the required voltage. There is a difference among the even and odd numbers of the modular structure which linked in cascaded connection in switch configuration. In case the switching configuration is not correct the output will not be the require.

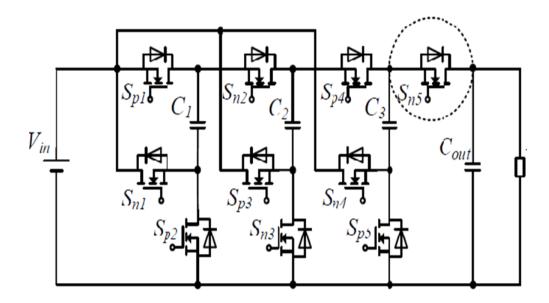


Figure 2.6: Three stage of the IMMCCC

# 2.9 High voltage converter structures

For obtaining a high value of voltage conversion percentage with a high level of efficiency, several high gain improvement methods were investigated. Each method has its special benefits and restrictions. The switched capacitor DC-DC converter could accomplish a high value of efficiency but has poor regulation capability and pulsating current. Introduction of a resonant switched capacitor converter could lessen the pulsating current but does not tackle the regulation problem [37]. Several applications are applied for getting high value of voltage accompanied with minimal of switches in order to increase efficiency value, they are explained as follows: A novel DC-DC converter topology is suggested which optimize the value of efficiency and lessen the value of voltage stress through the switches. DC-DC converters with a one switch are suggested that reduce the reverse recovery issues and losses resulted from switching. Transformer less DC-DC converters with a cascade method are suggested for reducing the current stresses and voltage values through the switches. Voltage multiplier with the interleaved converter is suggested in power administration solutions like EV. DC-DC converter with a coupled inductor is suggested for gaining high value voltage [38]. A soft method of switching in collaboration with PFC controller and pulse width modulation is presented for attaining high level of energy conversion efficiency with improved PF [39].

# 2.10 Marx Topology DC-DC Boost Converter

The structure is capable of producing a high voltage gain. [40]. And this converter considered one of the multilevel converters. The output voltage has a proportional relationship with a number of stage and duty cycle [8]. Figure 2.7 explains three stages of converter. From Figure 2.7, each converter's stage consists of 2 levels DC-DC boost converter [41]. The converter has been built on the Marx impulse generator principle where high dc voltage production from low dc voltage [42]. This concept is obtained by parallel charging of the capacitors and series discharging for the same capacitors. So through this configuration of capacitors, produces a high voltage from low input voltage [9].

Many traditional 2-level DC-DC boost converters will connect at the input side in parallel to minimize conduction losses and copper losses, while series connection will apply at the output side to minimize the voltage rating of stage capacitors. Moreover, the voltage stress of the lower switch device at the input side is the same at the output side. Therefore, low on-resistance semiconductors will implement in the structure. On the other hand, high voltage rating diodes will be used on the input side. However, The SiC diodes that characteristics such as low forward voltage and low reverse recovery time use at the present. That is lead to a high-efficiency boost converter [8].

#### **REFERENCES**

- [1] S. S. Dash and B. Nayak, "Control analysis and experimental verification of a practical dc-dc boost converter," *J. Electr. Syst. Inf. Technol.*, vol. 2, no. 3, pp. 378–390, 2015.
- [2] J. Liu, W. Ming, and F. Gao, "A New Control Strategy for Improving Performance of Boost DC / DC Converter Based on Input-Output Feedback Linearization," no. 1, pp. 2439–2444, 2010.
- [3] M. Elsied, A. Oukaour, H. Chaoui, H. Gualous, R. Hassan, and A. Amin, "Real-time implementation of four-phase interleaved DC-DC boost converter for electric vehicle power system," *Electr. Power Syst. Res.*, vol. 141, pp. 210–220, 2016.
- [4] V. F. Pires, D. Foito, F. R. B. Baptista, and J. F. Silva, "A photovoltaic generator system with a DC / DC converter based 'uk topology on an integrated Boost-C," vol. 136, pp. 1–9, 2016.
- [5] C. Li *et al.*, "A high boost ratio bidirectional isolated DC-DC converter for wide range low voltage high current applications," *Conf. Proc. IEEE Appl. Power Electron. Conf. Expo. APEC*, pp. 539–546, 2012.
- [6] X. Hu and C. Gong, "A High Gain Input-Parallel Output-Series DC / DC," *IEEE Trans. Power Electron.*, vol. 30, no. 3, pp. 1306–1317, 2015.
- [7] Y. Hsieh, J. Chen, T. Liang, and L. Yang, "Novel High Step-Up DC-DC Converter for Distributed Generation System," *IEEE Trans. Ind. Electron.*, vol. 60, no. 4, pp. 1473–1482, 2013.
- [8] A. Bin Ponniran *et al.*, "Fundamental Operation of Marx Topology for High Boost Ratio DC-DC Converter," *IEEJ J. Ind. Appl.*, vol. 5, no. 4, pp. 329–338, 2016.
- [9] A. Bin Ponniran, K. Orikawa, and J. I. Itoh, "Interleaved high boost ratio Marx topology DC-DC converter," 2015 IEEE 2nd Int. Futur. Energy Electron. Conf. IFEEC 2015, 2015.
- [10] K. Jyotheeswara Reddy and S. Natarajan, "Energy sources and multi-input

- DC-DC converters used in hybrid electric vehicle applications A review," *Int. J. Hydrogen Energy*, pp. 1–22, 2018.
- [11] S. W. Mohod and A. V. Padgavhankar, "Digital controller of DC-DC converter for solar cell module," 2014 Int. Conf. Circuits, Power Comput. Technol. ICCPCT 2014, pp. 449–454, 2014.
- [12] A. T. Alexandridis and G. C. Konstantopoulos, "Modified PI speed controllers for series-excited dc motors fed by dc/dc boost converters," *Control Eng. Pract.*, vol. 23, no. 1, pp. 14–21, 2014.
- [13] J. Pereda and J. Dixon, "Sinusoidal multilevel converters for Electric Vehicle motors," in EVS 2010 Sustainable Mobility Revolution: 25th World Battery, Hybrid and Fuel Cell Electric Vehicle Symposium and Exhibition, 2010.
- [14] A. J. Sabzali, E. H. Ismail, and H. M. Behbehani, "High voltage step-up integrated double Boost-Sepic DC-DC converter for fuel-cell and photovoltaic applications," *Renew. Energy*, 2015.
- [15] W. Josias de Paula, D. de S. Oliveira Júnior, D. de C. Pereira, and F. L. Tofoli, "Survey on non-isolated high-voltage step-up dc-dc topologies based on the boost converter," *IET Power Electron.*, 2015.
- [16] K. Tseng, C. Chen, and C. Cheng, "A High-Efficiency High Step-Up Interleaved Converter with a Voltage Multiplier for Electric Vehicle Power Management Applications," vol. 16, no. 2, pp. 414–424, 2016.
- [17] H. Liu, J. Ai, and F. Li, "A Novel High Step-Up Converter with a Switched-Coupled-Inductor-Capacitor Structure for Sustainable Energy Systems," no. March 2016, 2017.
- [18] T. Liang, J. Lee, S. Chen, and J. Chen, "Novel Isolated High-step-up DC-DC Converter with Voltage Lift," no. c, 2011.
- [19] S. Yang *et al.*, "A Buck Power Factor Correction Converter with Predictive Quadratic Sinusoidal Current Modulation and Line Voltage Reconstruction," vol. 0046, no. c, pp. 1–9, 2016.
- [20] C. Srun, "A High Voltage Gain DC-DC Converter Design based on Charge Pump Circuit Configuration with a Voltage Controller," 2018 2nd Int. Conf. Appl. Electromagn. Technol., pp. 79–84, 2018.
- [21] J. Scofield, S. Mcneal, B. Jordan, and B. Ray, "Studies of Interleaved DC-DC Boost Converters Whit Coupled Inductors," p. 21, 2011.
- [22] J. Xue and H. Lee, "A 2MHz 12-to-100V 90%-efficiency self-balancing ZVS

- three-level DC-DC regulator with constant-frequency AOT V2 control and 5ns ZVS turn-on delay," in *Digest of Technical Papers IEEE International Solid-State Circuits Conference*, 2016.
- [23] B. Gu, J. Dominic, J. S. Lai, Z. Zhao, and C. Liu, "High boost ratio hybrid transformer DC-DC converter for photovoltaic module applications," *IEEE Trans. Power Electron.*, 2013.
- [24] S. Sivakumar, M. J. Sathik, P. S. Manoj, and G. Sundararajan, "An assessment on performance of DC-DC converters for renewable energy applications," *Renewable and Sustainable Energy Reviews*. 2016.
- [25] D. Rothmund, G. Ortiz, T. Guillod, and J. W. Kolar, "10kV SiC-based isolated DC-DC converter for medium voltage-connected Solid-State Transformers," in *Conference Proceedings IEEE Applied Power Electronics Conference and Exposition APEC*, 2015.
- [26] Y. Huang, C. Y. Lai, S. Xiong, S. Tan, and S. R. Hui, "Non-Isolated High-Step-Up Resonant DC / DC Converter."
- [27] K. A. Kanhav and M. Chaudhari, "A reliable multiple input DC-DC converter for hybrid power system," 2017 Second Int. Conf. Electr. Comput. Commun. Technol., pp. 1–7, 2017.
- [28] D. Patil, A. K. Rathore, and D. Srinivasan, "A non-isolated bidirectional soft switching current fed LCL resonant dc/dc converter to interface energy storage in DC microgrid," in *Conference Proceedings IEEE Applied Power Electronics Conference and Exposition APEC*, 2015.
- [29] M. Gowrinathan, V. Devi Maheswaran, and V. T. Sreedevi, "Design and implementation of SMR based bidirectional laptop adapter," *Lect. Notes Electr. Eng.*, 2015.
- [30] M.-H. Tsai, Y.-T. Chen, and R.-H. Liang, "DC-DC converter with high voltage gain and reduced switch stress," *IET Power Electron.*, 2014.
- [31] S. Padmanaban, A. Iqbal, and H. Abu-rub, "IMPLEMENTATION AND CONTROL OF EXTRA HIGH VOLTAGE DC-DC BOOST CONVERTER."
- [32] C. T. Pan, C. F. Chuang, and C. C. Chu, "A novel transformer-less adaptable voltage quadrupler DC converter with low switch voltage stress," *IEEE Trans. Power Electron.*, 2014.
- [33] M. V. Naik and P. Samuel, "Analysis of ripple current, power losses and high efficiency of DC-DC converters for fuel cell power generating systems,"

- Renew. Sustain. Energy Rev., vol. 59, pp. 1080–1088, 2016.
- [34] T. Babu and A. A. Balakrishnan, "Quadrupler DC converter for high intensity discharge lamp applications," *IEEE Int. Conf. Circuit, Power Comput. Technol. ICCPCT 2015*, pp. 1–6, 2015.
- [35] L. Sun, F. Zhuo, F. Wang, and T. Zhu, "A novel topology of high voltage and high power bidirectional ZCS DC-DC converter based on serial capacitors," in Conference Proceedings IEEE Applied Power Electronics Conference and Exposition APEC, 2016.
- [36] M. Pereira, A. Zenkner, and M. Claus, "Characteristics and benefits of modular multilevel converters for FACTS," in CIGRÉ SC B4 Session 2010, 2010.
- [37] B. Wu, S. Li, and Y. Liu, "A New Hybrid Boosting Converter for Renewable Energy Applications," vol. 31, no. 2, pp. 1203–1215, 2016.
- [38] N. Krishna, M. Prashanth, N. K. Kumari, D. S. G. Krishna, and M. P. Kumar, "ScienceDirect ScienceDirect Transformer Less High Voltage Gain Step-Up DC-DC Converter Transformer High Voltage Gain Converter The Less Using Cascode Technique 1 Cascode Technique Using Assessing the feasibility of using the heat temperature function for ," *Energy Procedia*, vol. 117, pp. 45–53, 2017.
- [39] G. Esfandiari, H. Aran, and M. Ebrahimi, "Compherensive Design of a 100 kW / 400 V High Performance AC-DC Converter Small Signal Modelling and," pp. 417–429, 2015.
- [40] P. W. Lehn, B.-T. Ooi, and E. Veilleux, "Marx dc–dc converter for high-power application," *IET Power Electron.*, vol. 6, no. 9, pp. 1733–1741, 2013.
- [41] A. Ponniran, K. Orikawa, and J. I. Itoh, "Modular multi-stage Marx topology for high boost ratio DC/DC converter in HVDC," *INTELEC, Int. Telecommun. Energy Conf.*, vol. 2016-Septe, 2016.
- [42] S. Zabihi, Z. Zabihi, and F. Zare, "A solid-state marx generator with a novel configuration," *IEEE Trans. Plasma Sci.*, vol. 39, no. 8, pp. 1721–1728, 2011.
- [43] R. G. Kanojiya and P. M. Meshram, "Optimal tuning of PI controller for speed control of DC motor drive using particle swarm optimization," 2012 Int. Conf. Adv. Power Convers. Energy Technol. APCET 2012, no. Dc, 2012.
- [44] M. Helmy, B. Mohammad, S. Aizam, and B. Zulkifli, "Total Harmonic Distortion (THD) for Current Control Using Three Phase Inverter."

- [45] M. A. Abdullah, C. W. Tan, and A. H. M. Yatim, "A Simulation Comparison of PI and Linear Quadratic Regulator Controllers in DC-DC Converter," 2015 *IEEE Conf. Energy Convers.*, no. 1, pp. 37–41, 2015.
- [46] K. F. Krommydas and A. T. Alexandridis, "Design and Passivity-Based Stability Analysis of a PI Current-Mode Controller for dc/dc Boost Converters," 2014 Am. Control Conf., pp. 5067–5072, 2014.
- [47] T. H. E. Challenge, "Innovative Power Architecture for Data Center and Telecommunications Sites," pp. 1–4.
- [48] S. Abe, Y. Ishizuka, T. Ninomiya, S. Yang, M. Shoyama, and M. Kaga, "Prototype evaluation of over 10W/cm3 high power density converter for 400V-DC power distribution system in data center," *Proc. Int. Conf. Power Electron. Drive Syst.*, pp. 1280–1284, 2013.
- [49] R. Verma and S. Gupta, "A ZVZCS three-phase boost dc-dc converter distributing 400VDC in Telco and data centers to improve energy efficiency," pp. 598–604, 2014.
- [50] S. M. Lisy and M. Smrekar, "Three case studies of commercial deployment of 400V DC data and telecom centers in the EMEA region," *INTELEC, Int. Telecommun. Energy Conf.*, vol. 2016-Septe, pp. 1–6, 2016.
- [51] M. Szpek, B. J. Sonnenberg, and S. M. Lisy, "400VDC distribution architectures for central offices and data centers," pp. 1–6, 2014.
- [52] M. Al, J. Van, and H. Gualous, "DC/DC Converters for Electric Vehicles," *Electr. Veh. Model. Simulations*, no. June 2014, 2012.
- [53] K. C. Tseng and J. T. Lin, "High step-up DC/DC converter for fuel cell hybrid electric vehicles," *ISNE 2013 IEEE Int. Symp. Next-Generation Electron.* 2013, pp. 498–501, 2013.